

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10067151	FILING DATE 01/29/2002	CLASS 716	SUBCLASS 16	GAU 2825	EXAMINER DO
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\*\*APPLICANTS: Rupp Charlie; Garverick Timothy; Arnold Jeffrey;

\*\*CONTINUING DATA VERIFIED:

THIS APPLN CLAIMS BENEFIT OF 60/265,303 01/30/2001

YES TO

\*\* FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input checked="" type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	ATTORNEY DOCKET NO
Verified and Acknowledged Examiner's initials		TD	STRE-003/01US
TITLE : Design methodology for merging programmable logic into a custom IC			
U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drawn	Fig.s Drawn
TERMINAL		Primary Examiner	
DISCLAIMER		PREPARED FOR ISSUE	
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